



US005416663A

United States Patent [19]

Atkins

[11] **Patent Number:** 5,416,663[45] **Date of Patent:** May 16, 1995[54] **ARRANGEMENT FOR PROTECTING
TELECOMMUNICATIONS EQUIPMENT
FROM VOLTAGE TRANSIENTS**[75] **Inventor:** Ian P. Atkins, Swindon, England[73] **Assignee:** Raychem Limited, London, England[21] **Appl. No.:** 291,782[22] **Filed:** Aug. 17, 1994**Related U.S. Application Data**

[63] Continuation of Ser. No. 30,307, May 13, 1993, abandoned.

[30] **Foreign Application Priority Data**

Sep. 28, 1990 [GB] United Kingdom 90 21 222.6

[51] **Int. Cl.⁶** H02H 9/04[52] **U.S. Cl.** 361/119; 361/111;
361/113[58] **Field of Search** 361/111, 119, 56, 119[56] **References Cited****U.S. PATENT DOCUMENTS**

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[57] **ABSTRACT**

An arrangement for protecting an electrical circuit such as a telephone circuit from a voltage transient comprises a voltage clamp or foldback device (3) arraigned to be connected between a current-carrying line (1) and ground (1'), a capacitor (2) in series with it, and an inductance (4) series connected in the line. During normal operation the arrangement acts as an LC filter whose high frequency break-point is determined by the capacitance of the voltage clamp or foldback device (3) and the value of the inductance (4) which are chosen to give a break point of 1 kHz to 10 MHz. However, when the arrangement is subjected to a voltage transient, the voltage clamp or foldback device (3) fires causing the break-point to fall to a value determined by the values of the capacitor (2) and inductance (4). The arrangement may be used, for example, to protect the circuit against a rapid voltage drop caused by the firing of another circuit protection device such as a gas discharge tube 5.

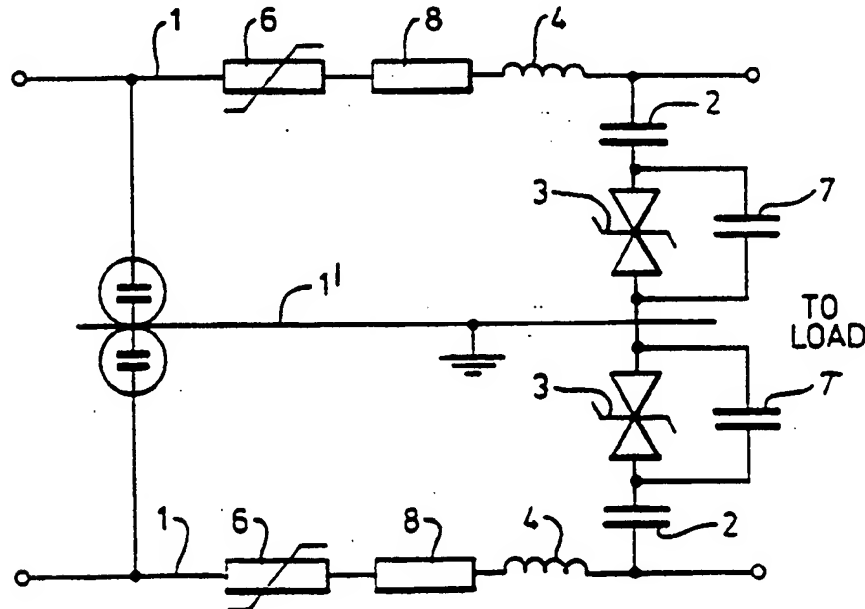
10 Claims, 2 Drawing Sheets

Fig.1.

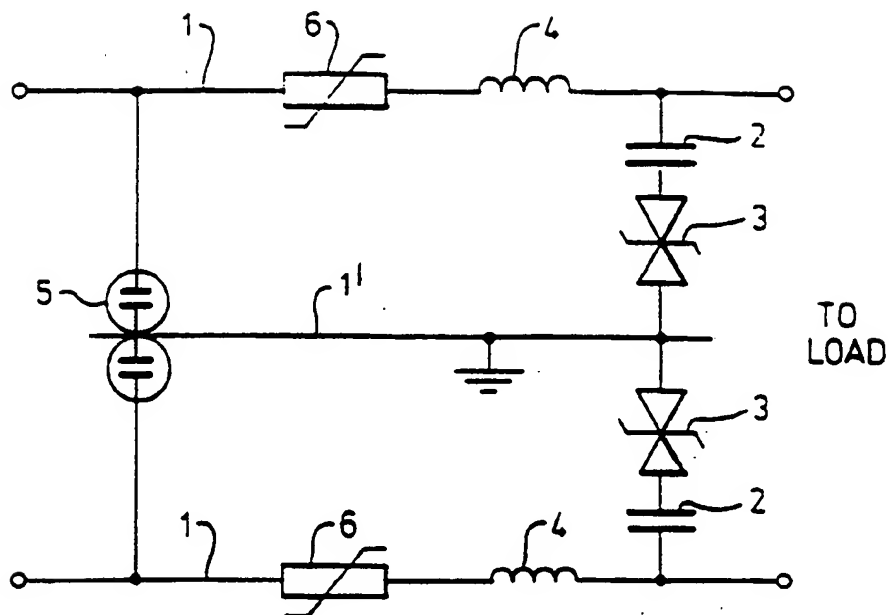


Fig.2.

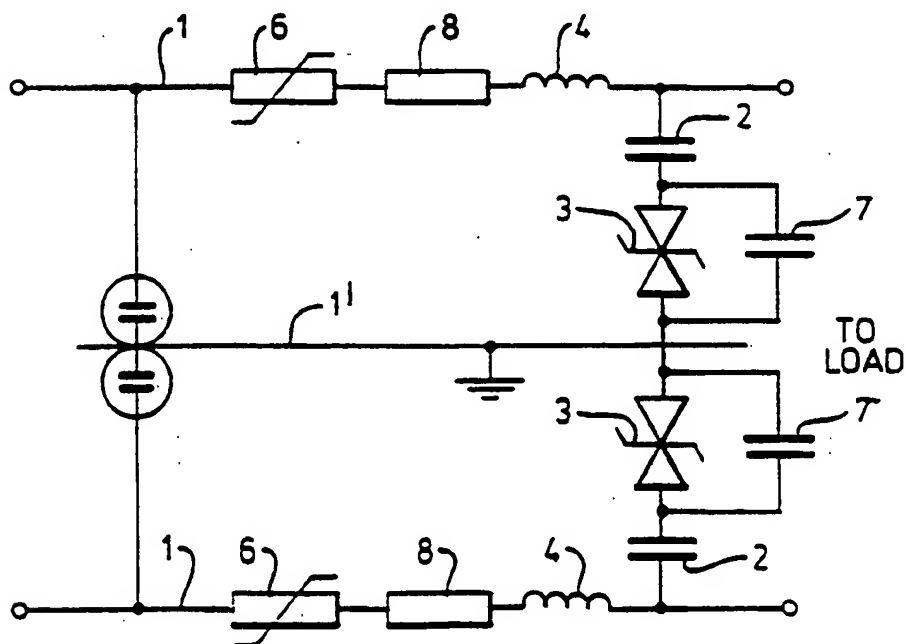


Fig.3.

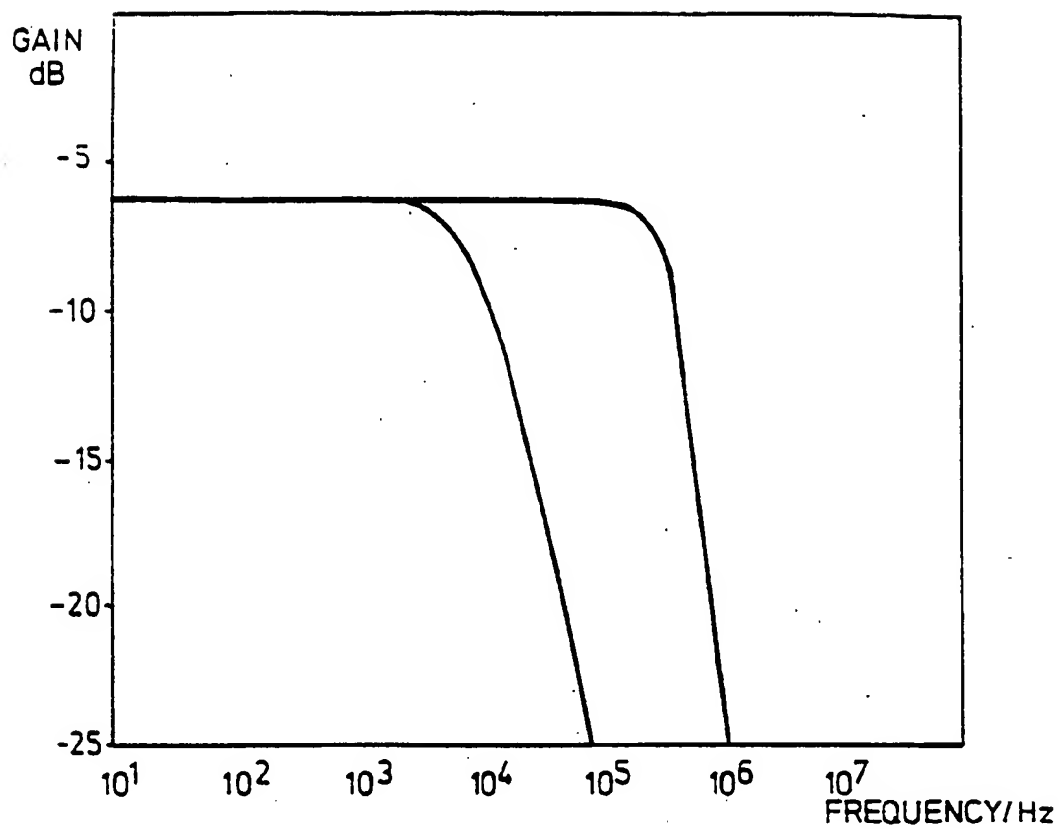
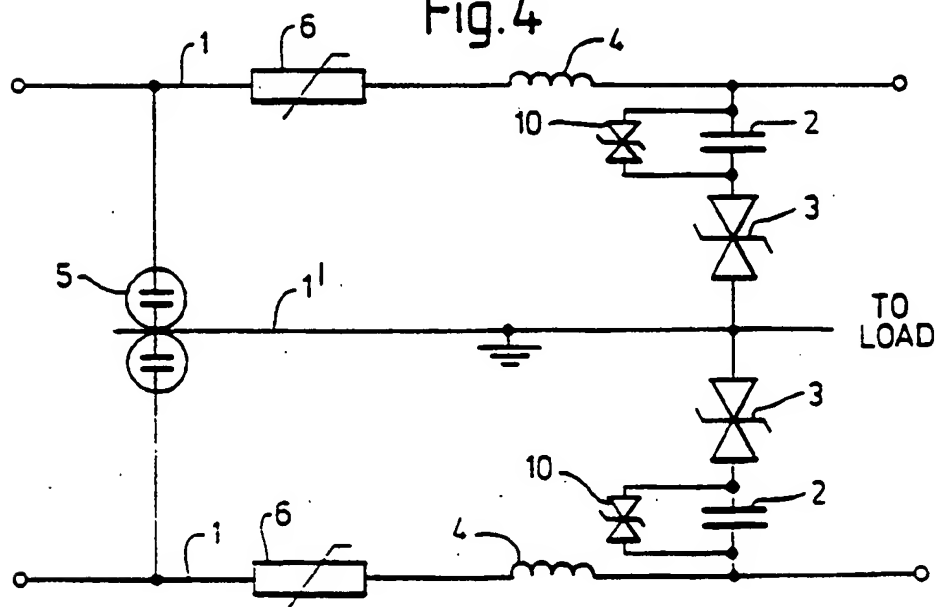


Fig.4



ARRANGEMENT FOR PROTECTING TELECOMMUNICATIONS EQUIPMENT FROM VOLTAGE TRANSIENTS

CROSS-REFERENCE TO RELATED APPLICATIONS

This application is a continuation of commonly assigned application Ser. No. 08/030,307, filed May 13, 1993, now abandoned.

BACKGROUND OF THE INVENTION

1. Field of the Invention

This invention relates to the protection of telecommunications equipment from voltage transients.

2. Introduction to the Invention

Voltage transients may be produced in electrical circuits by a variety of methods, for example by lightning, electrostatic discharge or pulses generated by operation of electrical equipment. These phenomena may all induce very large currents on cables and structures such as, for example telecommunications equipment, aircraft, and ships which can penetrate and damage electrical systems, either causing hardware damage such as semiconductor damage or electronic upset such as transmission loss or loss of stored data.

Electrostatic discharges are extremely fast discharges from a capacitor such as a human body. These discharges, because they can be so local, present a great threat to the individual electronic components. Induced electrical transients caused by lightning represent a threat to electrical/electronic equipment especially in telecommunications equipment.

SUMMARY OF THE INVENTION

According to the present invention, there is provided an arrangement for protecting telecommunications equipment from a voltage transient, which comprises a voltage clamping or foldback device connected or arranged to be connected between a current-carrying line of the circuit and ground, a capacitor having a capacitance up to 1 μF connected in series with the voltage clamping device, and an inductance of from 1 μH to 100 mH series connected in the line, and a high energy foldback device connected or arranged to be connected between the current-carrying line and ground, under normal operating conditions the arrangement acting as an LC filter whose high frequency break-point is determined by the capacitance of the voltage clamping device and any additional capacitor optionally connected in parallel therewith, and the inductance and is in the range of from 1 kHz to 10 MHz, preferably from 10 kHz to 5 MHz, the high frequency break-point falling when the arrangement is subjected to a voltage transient sufficient to cause the voltage clamping device to conduct, to a value that is determined by the values of the capacitor and the inductance.

DETAILED DESCRIPTION OF THE INVENTION

The voltage clamping device may be selected from a number of devices that have voltage limiting properties, for example a back-to-back diode arrangement in which the diodes may comprise semiconductor diodes, avalanche diodes or Zener diodes. Alternatively, non-linear resistors may be employed such as zinc oxide, silicon carbide or other varistor materials. The voltage clamping device may, if desired, comprise a MIM or nin

device or a gaseous voltage regulator. MIM 2rid nin devices that may be employed in the invention are described in our International Applications Nos. PCT/GB89/00809 and PCT/GB89/00808.

The preferred voltage clamping devices are bidirectional damping diodes, for example as sold under the tradename TRANSZORB. The clamping devices will normally have a stray capacitance in the range of from 10 pF to 10 nF and especially from 100 pF to 5 nF. In some instances the stray capacitance of the voltage clamping device may be too low, in which case a small capacitor may be connected in parallel with it to bring the resultant capacitance within the preferred range.

As stated above, the inductance has a value, in the range of from 1 μH to 100 mH preferably it has a value of up to 1 mH. This may, for example be achieved by utilizing the stray inductance of a wire wound resistor. In some circumstances it may be preferable instead to employ a small inductor. The inductance will normally have an associated resistance of from 1 to 50 ohms, especially from 3 to 10 ohms. The resistance may be chosen so that the total resistance of all series connected components in the line is within acceptable limits.

The capacitor has a capacitance of up to 1 mF, and preferably has a capacitance of at least 100 pF, especially at least 10 nF. In most cases it will be preferred for the capacitor to have a capacitance that is at least ten times the stray capacitance of the voltage clamping device, and preferably at least 100 times the stray capacitance. Where the capacitance is 100 times that of the voltage clamping device the high frequency break point should fall when subjected to a transient, to about one tenth the frequency of the break point under normal operating conditions.

The arrangement according to the invention has the advantage that under normal operating conditions it acts as a low pass filter with a relatively high break point, but that when it is subjected to a transient its high frequency gain is immediately reduced significantly. Thus, for example, where the break point falls to one tenth of its original frequency the gain magnitude at frequencies above the normal operating break point will be reduced by 40 dB. The high-energy foldback device employed in the arrangement is intended to protect the circuit against higher energy, longer duration overvoltages. It may, for example, comprise a spark gap or gas discharge tube (GDT) or a voltage controlled triac, connected between the conductor and ground for shunting high energy transients to ground. Such components can, in some circumstances, cause damage to the circuit they are used to protect by forming pulses having very rapidly changing voltages. For example, if a GDT is subjected to a transient that has a rising edge of 1 kV μs^{-1} it may switch at, say, 500 V whereupon the transient voltage falls to zero in about 1 ns, thereby causing the transient that is transmitted to have a falling edge of 500 kV μs^{-1} . Such a rate of change of voltage can cause permanent damage to the circuit load. However, in the present invention, the reduction in the break point frequency caused by switching of the voltage clamp will reduce the slope of the falling edge of that part of the transient that is let through the arrangement.

In addition the arrangement may include an overcurrent protection device series connected in the line between the additional, higher energy overvoltage device and the voltage clamping device in order to protect the circuit from long lasting overvoltages, for example

caused by system failure, or by accidental or malicious connection of mains supply. Suitable devices include positive temperature coefficient (PTC) devices, eg. ceramic or polymeric PTC devices, preferably polymeric devices, for example as described in U.S. Pat. Nos. 4,238,812 and 4,329,726 to Middleman et al. Such devices are available from Raychem Corporation under the registered trademark "POLYSWITCH". Since the circuit acts as a low pass filter both in its unswitched and switched state, it has the disadvantage that it cannot protect the circuit from DC or very low frequency overvoltages that are not great enough to cause the GDT or PTC device to switch. This may be overcome by connecting an additional voltage clamp or a foldback device across the capacitor. In this case the voltage clamp in series with the capacitor and the clamp or foldback device connected across the capacitor should have a combined clamping or threshold voltage that is greater than the normal equipment voltage. The term "foldback device" as used here is intended to mean a device which exhibits a high electrical resistance, e.g. at least 100 kohms, preferably at least 1 Mohms, at low electrical voltages but whose resistance falls from a high value to a low value, e.g. less than 10 ohms, when subjected to a high voltage (the lowest such voltage being referred to as the "threshold voltage"). The device will remain in its low resistance state only for as long as a small "holding current" is maintained through the device, otherwise it will automatically revert to its high resistance state. Suitable foldback devices include voltage controlled triacs, voltage controlled thyristors, and gas discharge tubes.

The arrangement according to the invention may be employed in telephone equipment where, for example, a balanced pair of lines having a common ground is used. In this case a circuit protection unit may be employed which comprised a pair of arrangements according to the invention arranged to be series connected in a pair of current-carrying lines and connected to a common ground.

BRIEF DESCRIPTION OF THE DRAWINGS

Two forms of arrangement according to the invention will now be described by way of example with reference to the accompanying drawings in which:

FIG. 1 is a circuit diagram of one form of arrangement in accordance with the invention;

FIG. 2 is a diagram of the circuit of FIG. 1 with some of the parasitic values shown;

FIG. 3 is a Bode diagram showing the performance of the circuit of FIG. 1; and

FIG. 4 is a circuit diagram of a modification of the circuit shown in FIG. 1.

DETAILED DESCRIPTION OF THE DRAWING

Referring to FIG. 1 of the accompanying drawings, a five terminal telephone circuit protection module has a balanced pair of lines 1 and a common ground line 1'. Two identical circuit protection arrangements are provided, one associated with each line of the module. The arrangements each comprise a bidirectional voltage clamping diode 3 connected between the line 1 and ground 1', a 100 nF capacitor 2 connected in series with the clamping diode 3, and a 200 μ H inductor 4 in series connected in the line. In addition, the arrangement includes a GDT 5 connected between the line 1 and ground, and a PTC device 6 series connected in the line. The PTC device 6 has a low temperature resistance of

about 6 ohms so that the total line resistance of the module is 6 ohms plus the resistance of the inductor 4.

FIG. 2 shows the circuit of FIG. 1 and indicates the most important stray quantities, namely the stray capacitance 7 of the voltage clamping device 3 and the resistance 8 of the inductor 4. Capacitance 7 is approximately 2 nF and resistance 8 is 5.6 ohms.

The performance of the arrangement is shown in FIG. 3. In normal operation the arrangement acts as a low pass RLC filter whose capacitance is given by the series connected capacitor 2 and stray capacitance of the clamping diode 3. When the unit is subjected to a voltage transient the clamping diode 3 will clamp the voltage between the line 1 and ground, with the result that the capacitance of the filter circuit is given by capacitor 2 above. The break point of the filter is therefore reduced to about one tenth the frequency of the original break point and the gain at frequencies above the normal operating break point is reduced by about 40 dB. When the GDT fires, the voltage on the line 1 will fall to zero within about 1 ns which corresponds to a voltage reduction of about 500 kV μ s⁻¹. The reduced frequency band of the filter will, however, limit the rate of fall of the voltage at the output of the unit to about 500 V μ s⁻¹.

FIG. 4 shows a modification of the circuit shown in FIG. 1 which will provide protection to a circuit against DC overvoltages. This circuit corresponds to that shown in FIG. 1 with the addition of a further bidirectional voltage clamping diode 10 connected across the capacitor 2. The clamping diode 10 has a breakdown voltage such that the sum of the breakdown voltages of the two diodes 3 and 10 is greater than the minimum system voltage.

I claim:

1. An arrangement for protecting telecommunications equipment from a voltage transient, which comprises

- (1) a voltage clamping device having a capacitance and connected between a current-carrying line of a circuit comprising the telecommunications equipment and ground,
- (2) a capacitor having a capacitance value up to 1 μ F connected in series with the voltage clamping device,
- (3) an inductance having a value of from 1 μ H to 100 mH a series connected in the line, and
- (4) a high-energy foldback device connected between the current-carrying line and ground,

under normal operating conditions the arrangement acting as an LC filter whose high frequency break-point

- (a) is determined by (i) the capacitance of the voltage clamping device; (ii) any additional capacitor optionally connected in parallel with the voltage clamping device and the inductance, and (iii) is in the range of from 1 kHz to 10 MHz, and
- (b) falls when the arrangement is subjected to a voltage transient sufficient to cause the voltage clamping device to conduct, to a value that is determined by the values of the capacitor and the inductance.

2. An arrangement as claimed in claim 1, wherein the inductance has a value of up to 1 mH.

3. An arrangement as claimed in claim 1, which has a total series resistance in the range of from 1 to 50 ohms.

4. An arrangement as claimed in claim 4, which has a total series resistance in the range of from 3 to 10 ohms.

5. An arrangement as claimed in claim 1, wherein the high frequency break point, when the arrangement is

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subjected to a voltage transient sufficient to cause the voltage clamping device to conduct, occurs at a frequency not higher than 0.1 times the frequency of the break-point under normal operation.

6. An arrangement as claimed in claim 5, wherein the high energy foldback device is a gas discharge tube or a voltage controlled triac.

7. An arrangement as claimed in claim 1, which includes an overcurrent protection device series connected in the current-carrying line.

8. An arrangement as claimed in claim 7 wherein the overcurrent protection device is a PTC device.

9. An arrangement as claimed in claim 1, which includes an additional voltage clamping or foldback device connected across the capacitor, which capacitor is connected in series with the voltage clamping device and has a capacitance value up to 1 μ F.

10. A circuit protection unit which comprises two arrangements for protecting telecommunications equipment, each of which arrangements is series connected in a respective current-carrying line of a pair of current-carrying lines and each of which arrangements is con-

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nected to a common ground to form a circuit, each of the arrangements comprising

(1) a voltage clamping device having a capacitance and connected between a current-carrying line of the circuit and ground,

(2) a capacitor having a capacitance value up to 1 μ F connected in series with the voltage clamping device,

(3) an inductance having a value of from 1 μ H to 100 mH series connected in the line, and

(4) a high-energy foldback device connected between the current-carrying line and ground,

under normal operating conditions each arrangement acting as an LC filter whose high frequency break-point

(a) is determined by (i) the capacitance of the voltage clamping device, (ii) any additional capacitor optionally connected in parallel with the voltage clamping device and the inductances, and (iii) is in the range of from 1 kHz to 10 MHz,

falls when the arrangement is subjected to a voltage transient sufficient to cause the voltage clamping device to conduct, to a value that is determined by the values of the capacitor and the inductance.

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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 5,416,663

Page 1 of 2

INVENTOR(S) : Ian P. Atkins

DATED : May 16, 1995

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Cover Page, Foreign Application Priority Data [30], after "90 21 222.6" insert as a separate line --Sep. 27, 1991 PCT/GB91/01668--.

Column 1, line 28, replace "h-man" by --human--.

Column 1, line 39, delete "or foldback".

Column 1, line 61, replace "my" by --may--.

Column 2, line 1, replace "2rid" by --and--.

Column 2, line 6, replace "damping" by --clamping--.

Column 2, line 16, replace "100 mH" by --100 mH,--.

Column 2, line 28, replace "ton" by --ten--.

Column 2, line 43, after "40 dB." begin a new paragraph with "The".

Column 3, line 8, after "'POLY SWITCH'." begin a new paragraph with "Since".

Column 3, line 14, replace "en" by --an--.

Column 3, line 22, after "at least 1" insert --Mohm, especially at least 10--.

Column 3, line 64, replace "uH" by --μH--.

Column 4, line 4, replace "quantifies" by --quantities--.

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 5,416,663

Page 2 of 2

INVENTOR(S) : Ian P. Atkins

DATED : May 16, 1995

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4, lines 22 and 25, replace "us⁻¹" by -- μ s⁻¹--.

Column 4, line 26, replace "modification" by --modification--.

Column 4, line 34, replace "minimum" by --maximum--.

Claim 1, line 18, replace "device;" by --device,--.

Claim 10, lines 24 to 25, after "10 MHz," insert --and-- and before "falls" insert --(b)--.

Signed and Sealed this

Eighteenth Day of June, 1996

Attest:



BRUCE LEHMAN

Attesting Officer

Commissioner of Patents and Trademarks